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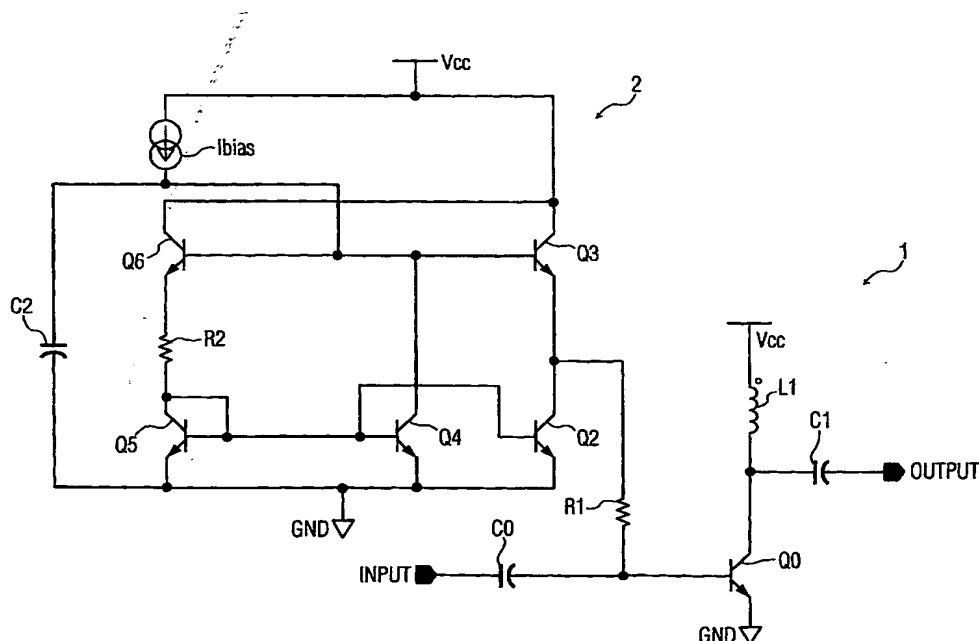
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(54) Title: **AMPLIFIER CIRCUIT HAVING AN EXTENDED WILSON CURRENT-MIRROR SELF-BIAS BOOSTING CIRCUIT**



(57) Abstract: An amplifier circuit (1) includes an amplifying transistor (Q0) and a do bias circuit (2) for biasing the amplifier transistor (Q0) to obtain a conduction angle of at least about 180°. The do bias circuit (2) includes a self-bias boosting circuit which has a Wilson current-mirror circuit (Q2-Q6) having an output coupled to a control terminal of the amplifying transistor (Q0) by a resistor (R1), and a capacitor (C2) coupled from the extended Wilson current-mirror circuit (Q2-Q6) to a common terminal (Gnd).

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